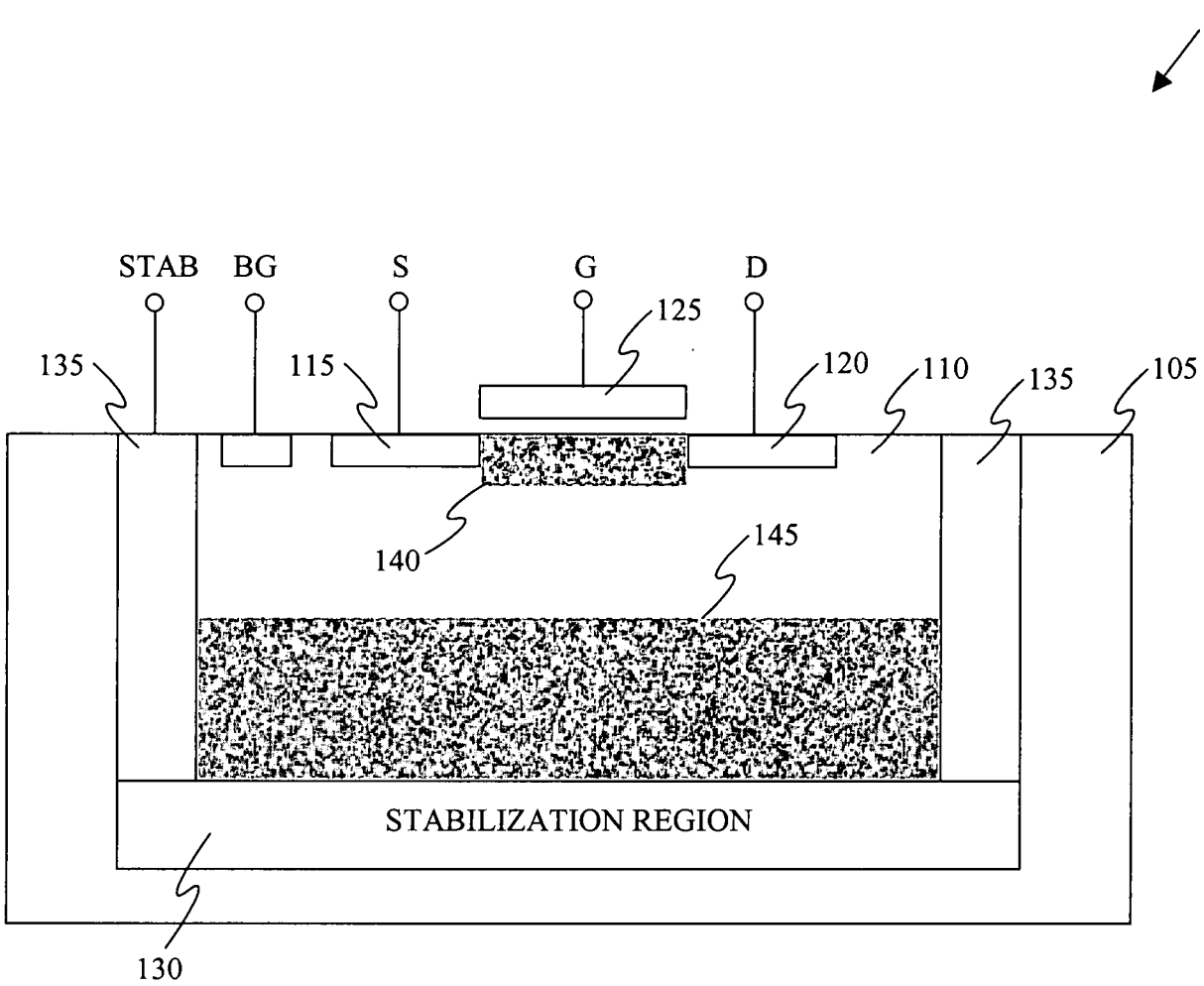
100
STAB
BG
S
G
D
125
135
115
120
110
135
105
140
145
STABILIZATION REGION
130

FIGURE 1A

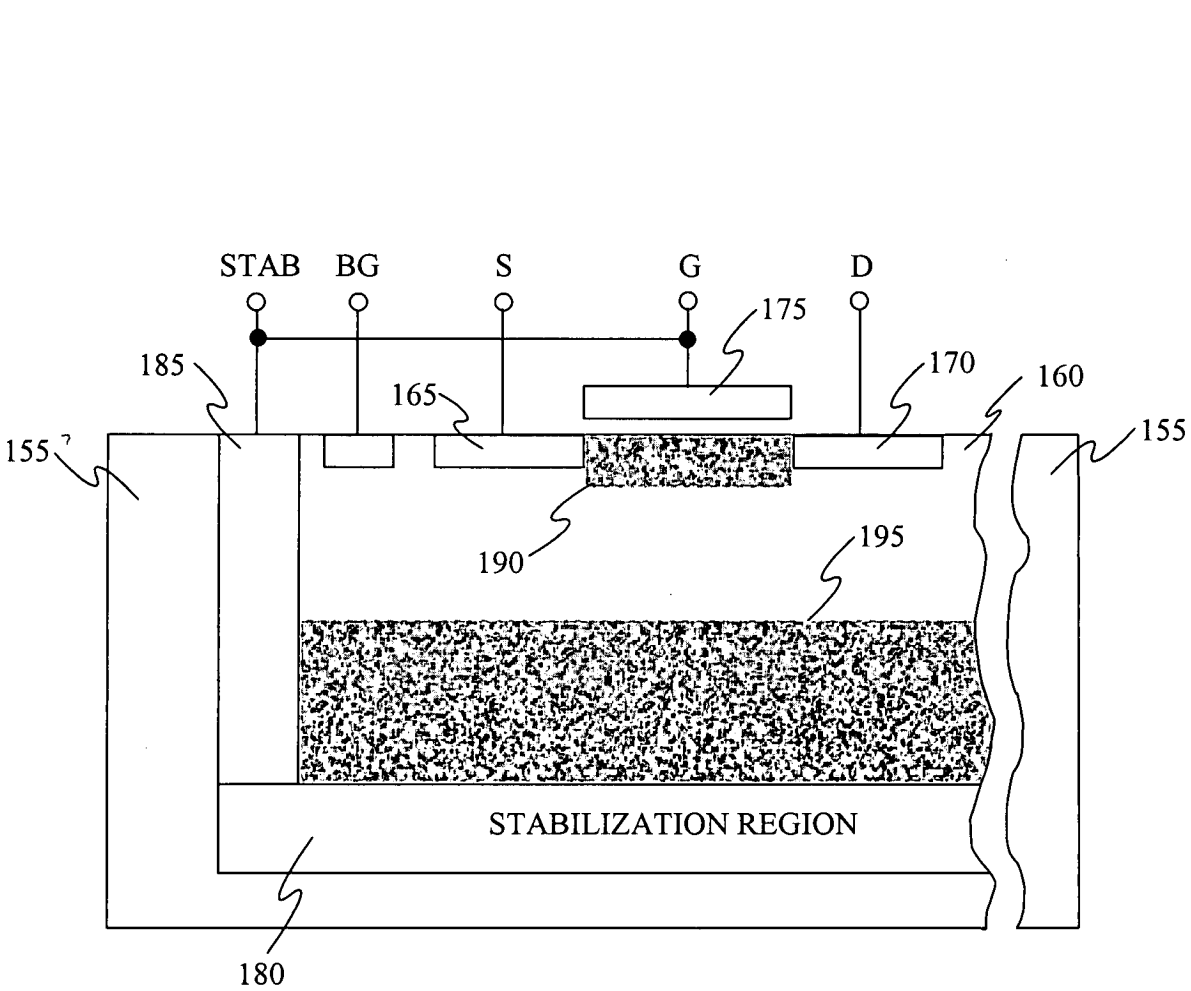
150
STAB
BG
S
G
D
175
185
165
170
160
155
155
190
195
STABILIZATION REGION
180

FIGURE 1B

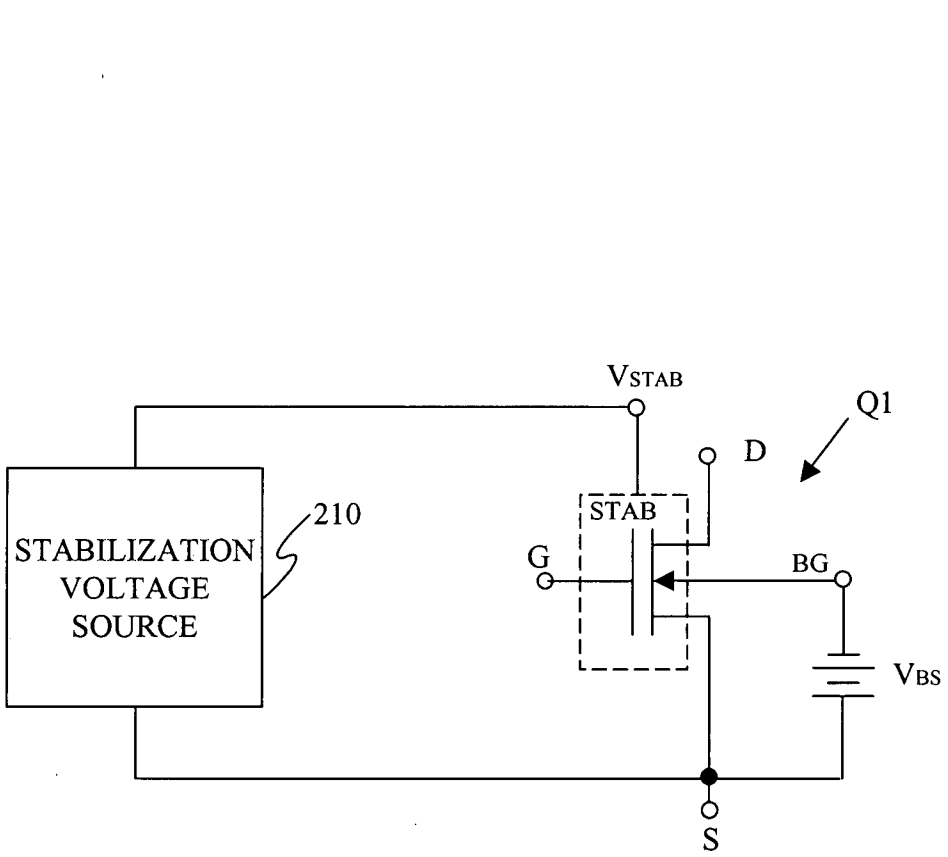
200
VSTAB
Q1
D
STABILIZATION
VOLTAGE
SOURCE
210
STAB
G
BG
VBS
S

FIGURE 2

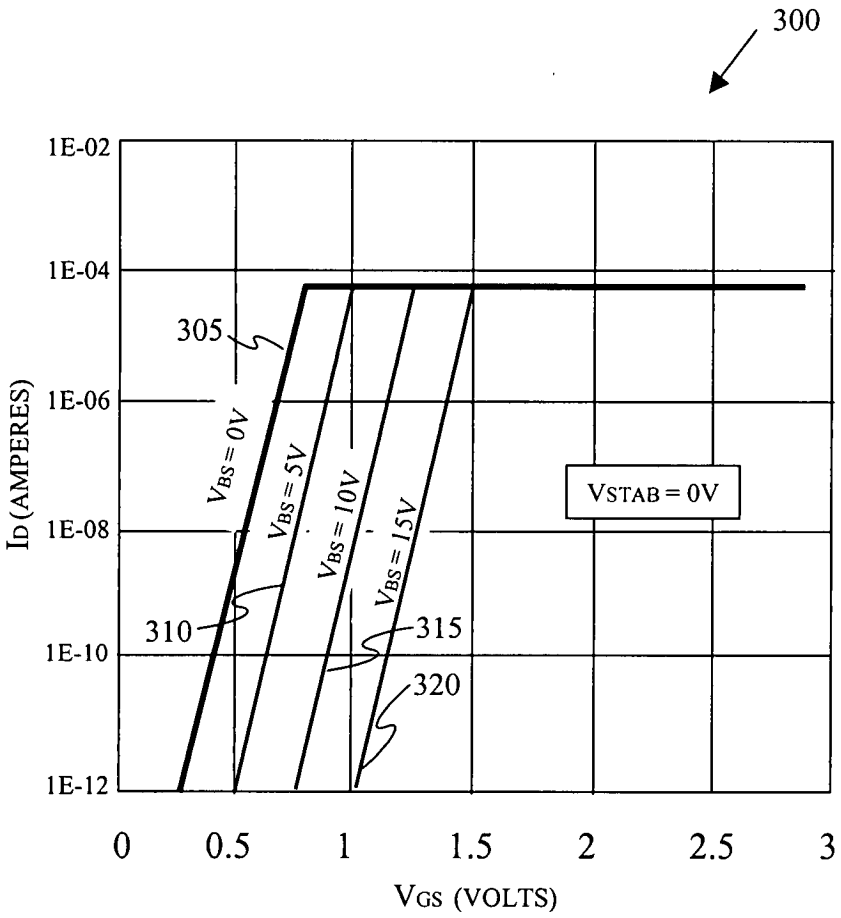

FIGURE 3A

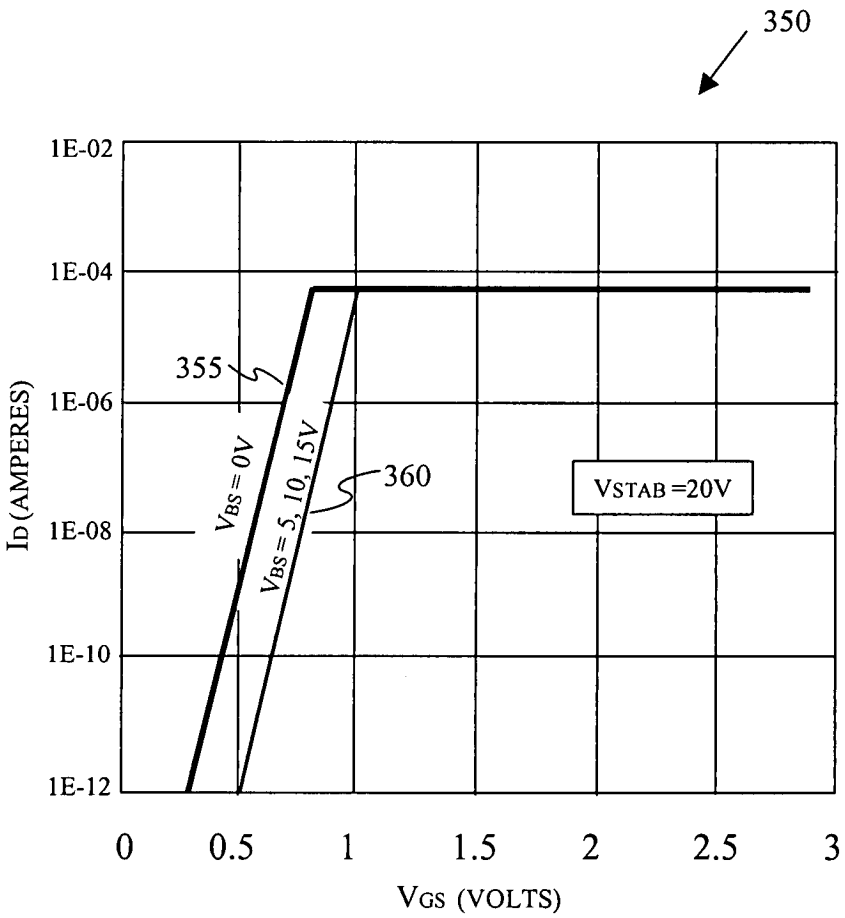

FIGURE 3B

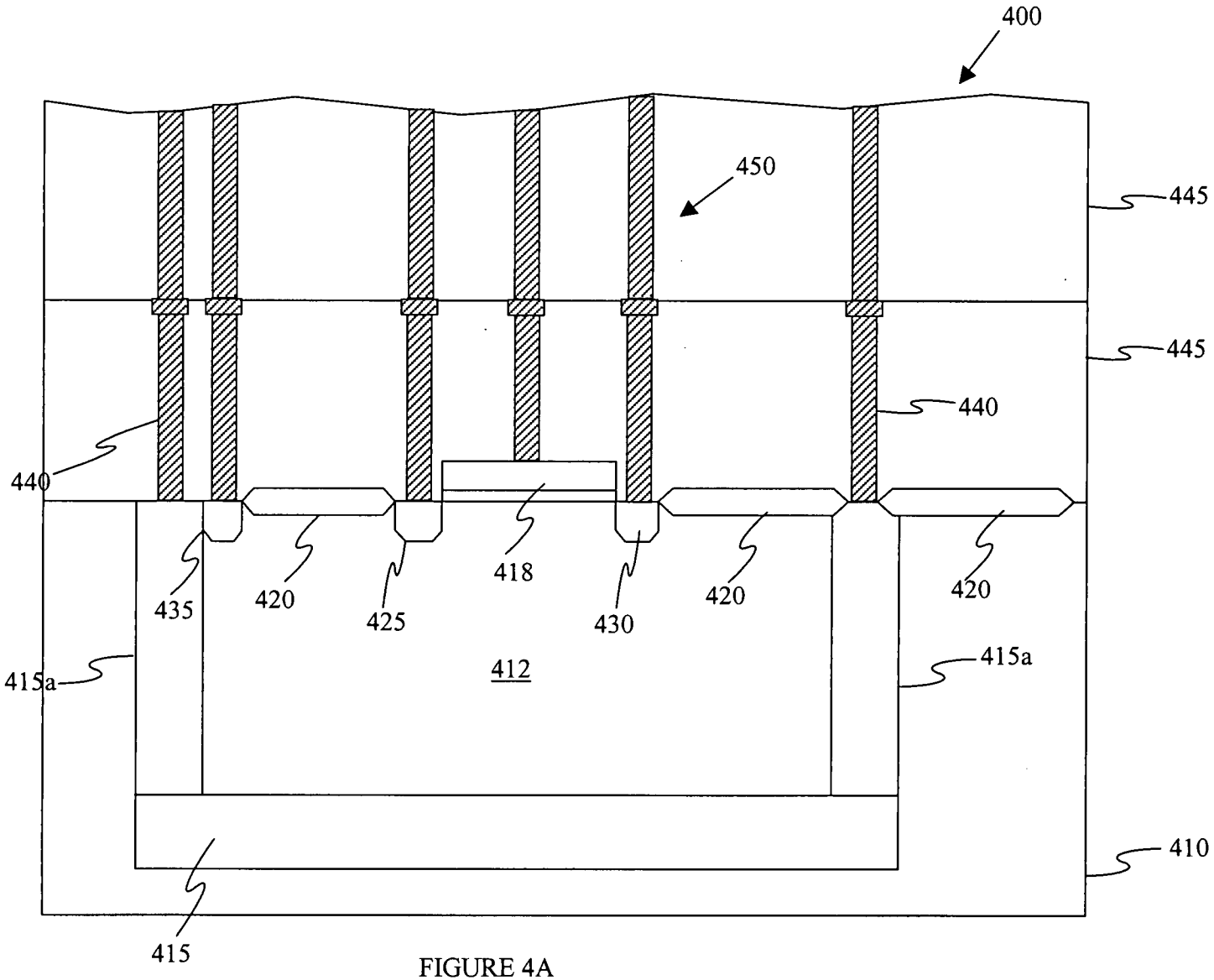
400
450
445
445
440
440
435
420
425
418
430
420
420
415a
412
415a
410
415

FIGURE 4A

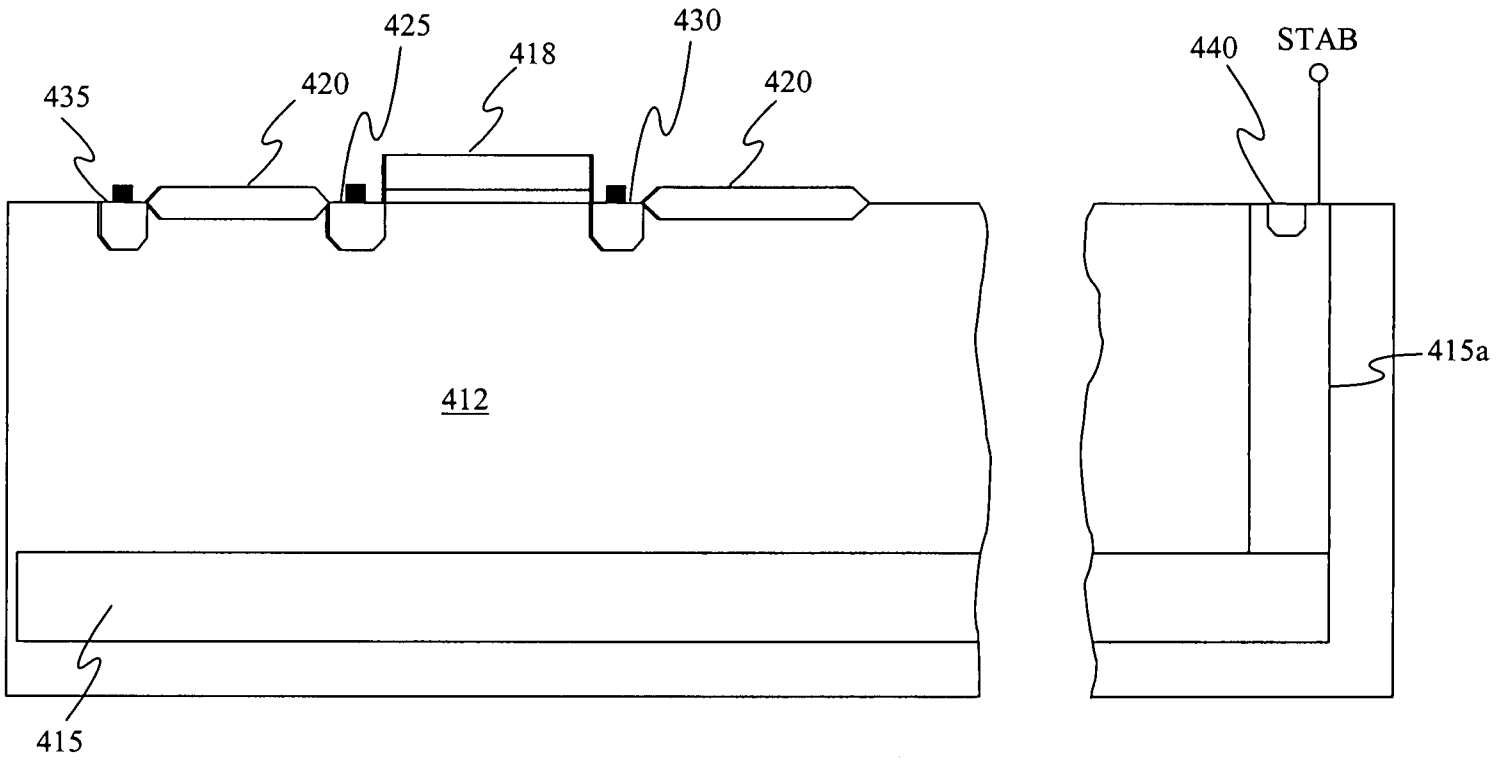

FIGURE 4B

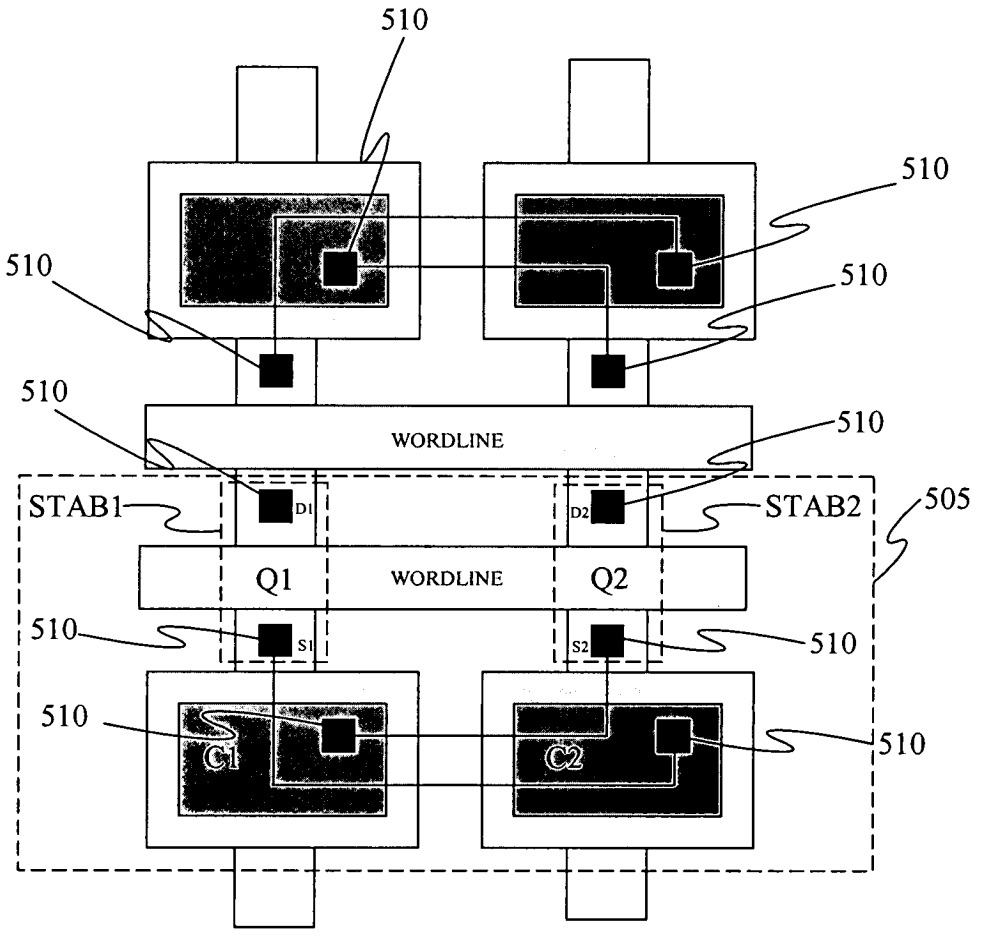
500
510
510
510
510
510
510
510
WORDLINE
STAB1
D1
D2
STAB2
505
Q1
WORDLINE
Q2
510
S1
S2
510
510
C1
C2
510

FIGURE 5

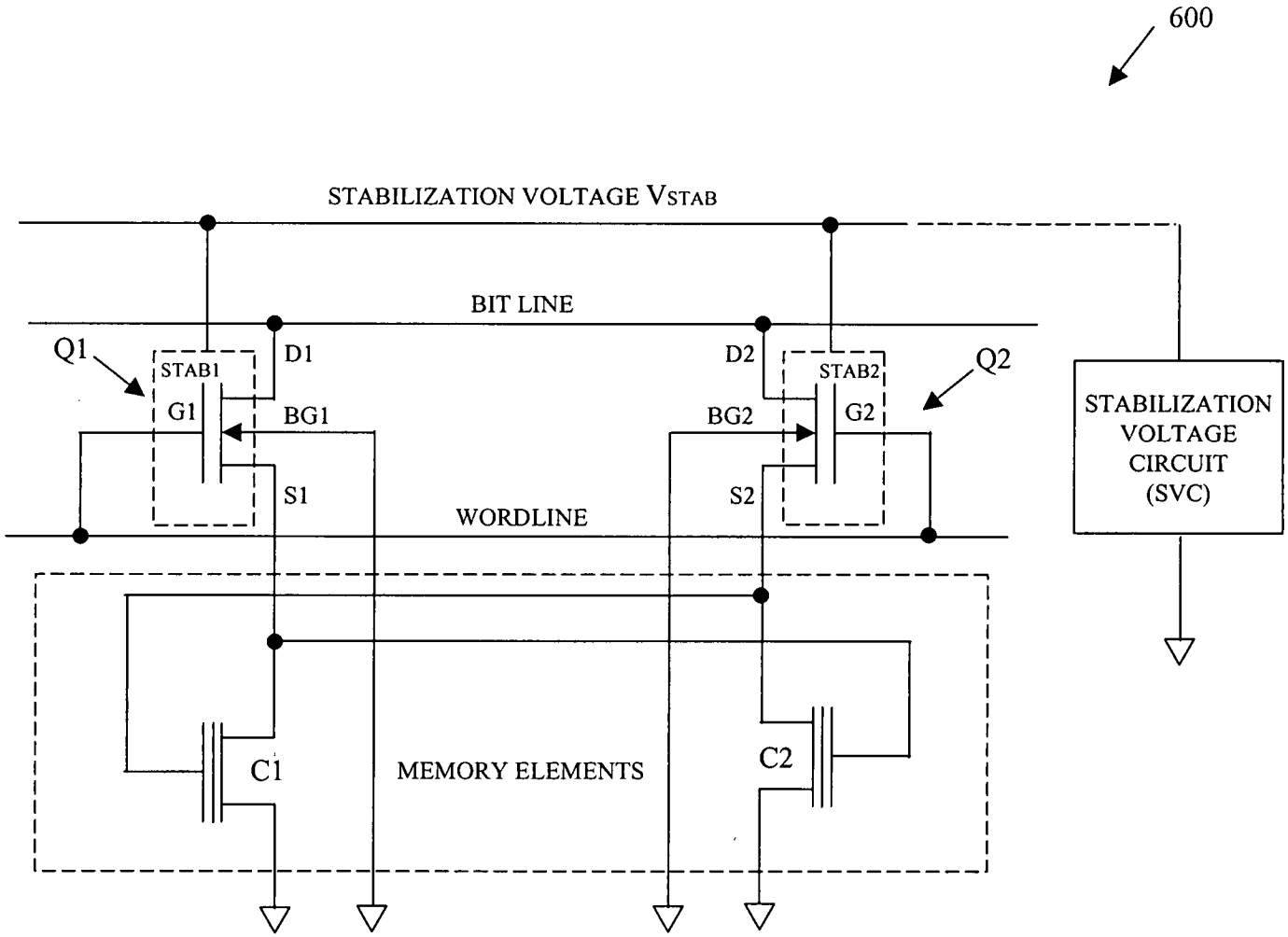
600
STABILIZATION VOLTAGE VSTAB
BIT LINE
Q1
STAB1
D1
G1
BG1
S1
WORDLINE
D2
STAB2
Q2
BG2
G2
S2
STABILIZATION VOLTAGE CIRCUIT (SVC)
C1
MEMORY ELEMENTS
C2

FIGURE 6